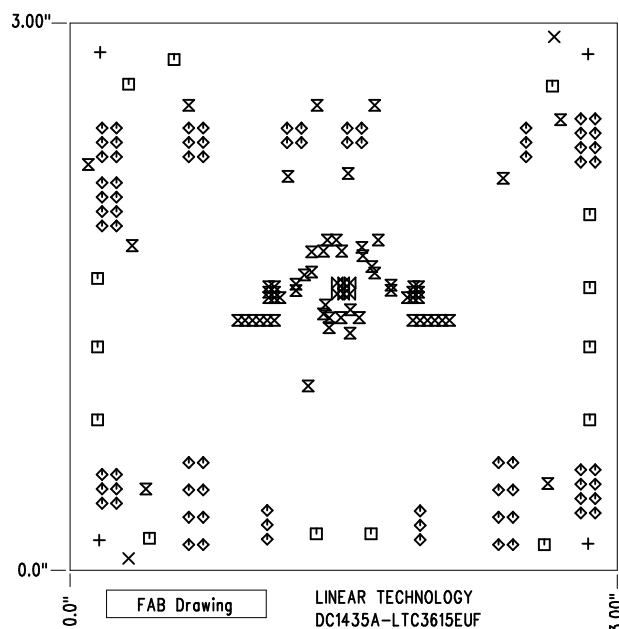


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	PRODUCTION	TOM	04-30-10



SIZE	QTY	SYM	PLATED	TOL
0.18701	4	+	NO	+/-0.003
0.07	2	X	NO	+/-0.003
0.095	14	□	YES	+/-0.003
0.035	75	◇	YES	+/-0.003
0.01	62	⊗	YES	+/-0.003
0.02	6	⊗	YES	+/-0.003

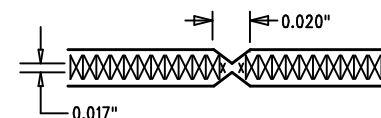
LINEAR TECHNOLOGY
DC1435A-LTC3615EUF
DUAL 3A MONOLITHIC SYNCHRONOUS
STEP-DOWN REGULATOR
DATE: 04-30-10


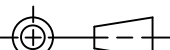
LAYERS STRUCTURE

■	LAYER 1 (COMPONENT SIDE)
■	LAYER 2
■	LAYER 3
■	LAYER 4
■	LAYER 5
■	LAYER 6 (SOLDER SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		<div> LINEAR TECHNOLOGY</div> <div>1830 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>		
DIMENSIONS ARE IN INCHES		PCB DES.	A.K.			
TOLERANCES:		APP ENG.	TOM G.			
0.XX" = ±0.01"						
0.XXX" = ±0.005"				TITLE: FABRICATION DRAWING DUAL 3A MONOLITHIC SYNCHRONOUS STEP-DOWN REGULATOR		
INTERPRET DIM AND TOL PER ASME Y14.5M-1994						
THIRD ANGLE PROJECTION						
		SCALE = NONE		SIZE N/A	IC NO. LTC3615EUF DEMO CIRCUIT 1435A	REV 2
		FILENAME: DC1435A-2.PCB		SHT 1 OF 1		